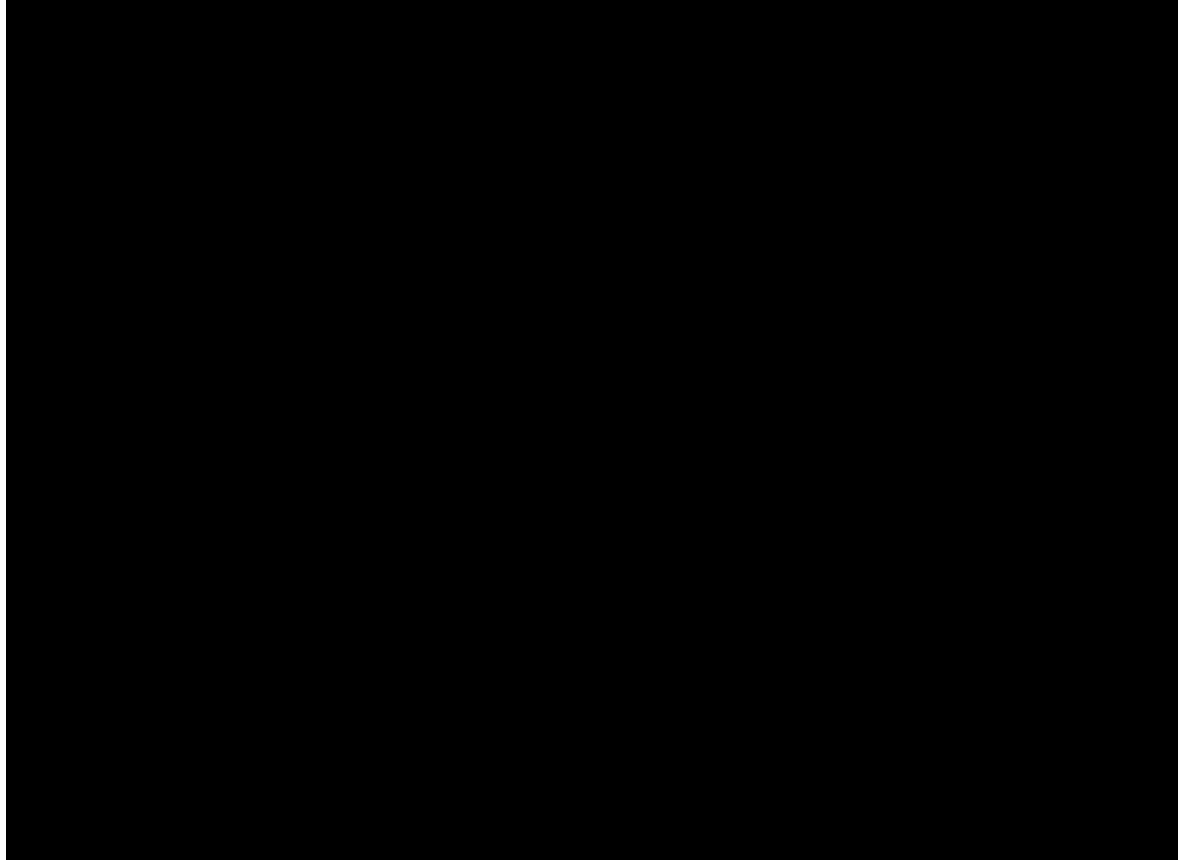


Efinity Debugger (Logic Analyzer)



Debugger Wizard



Debugger Wizard

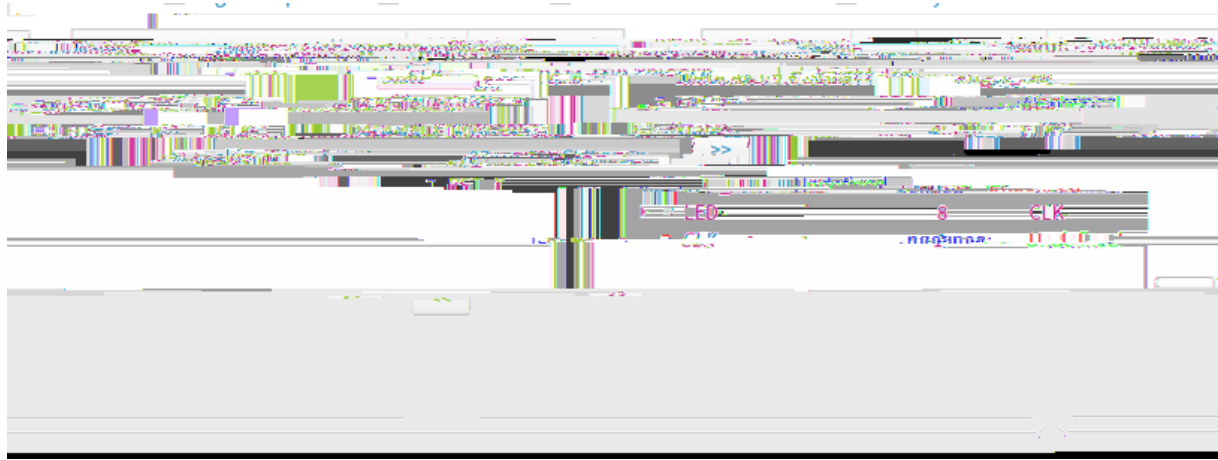


Debugger Wizard

- Signals from - Post-Map, elaborated (Pre-Map) Netlist



Debugger Wizard



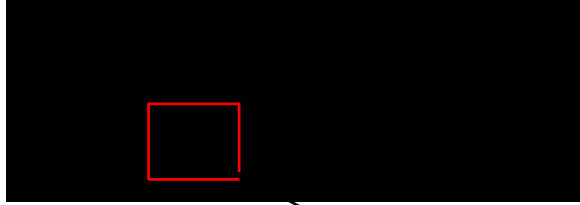
- - Debug
>>g m



Clock Domain -

Clock Source

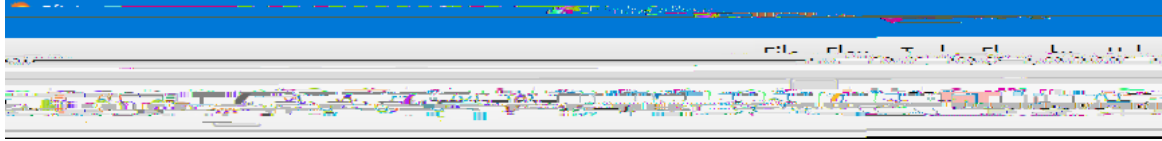




Wizard



Debugger Logic Analyzer



Debugger



Debugger - Trigger Setup ->



Debugger - Run



Debug

-

Efinity

~

Debug

'vcd (Value Change Dump)'





